

IN THE CLAIMS:

Claims 1-39. (Cancelled).

40. (New) A display apparatus comprising:  
a plurality of display systems each comprising a light source, a reflection type liquid crystal panel and an optical system;  
a screen displaying superimposingly the images obtained from said plurality of display systems;  
means for selecting at least one display system for displaying from said plurality of display systems;  
switching means for switching the selection of said display systems in said selecting means;  
an interface to at least one information terminal; and  
an image processing circuit section, wherein  
an optical axis of said optical system is inclined relative to a surface of said screen for displaying projected images, and a maximum distance between said screen and an exit pupil is  $b_2$ , and a minimum distance between said screen and the exit pupil is  $b_1$ , while a maximum distance between said liquid crystal panel and an entrance pupil of said optical system is  $a_2$ , and a minimum distance between said liquid crystal panel and the entrance pupil of said optical system is  $a_1$ , the inclination of said liquid crystal panel is so adjusted to minimize the difference between  $b_1/a_1$  and  $b_2/a_2$ .

41. (New) A display apparatus according to claim 40, wherein said liquid crystal panel is provided such that an electrode is disposed at an angle with a front surface side of a transparent substrate in which an illumination light is incident.

42. (New) A display apparatus according to claim 40, wherein said liquid crystal panel comprises a pixel section comprising a polycrystalline Si thin film transistor, and a peripheral circuit section comprising bulk Si transistor.

43. (New) A display apparatus according to claim 40, further comprising means for transmitting display image information to said information terminal.

44. (New) A display apparatus according to claim 40, wherein said image processing circuit section includes memory.

45. (New) A display apparatus according to claim 40, wherein said image processing circuit section comprises means for compressing, expanding, encoding and decoding.

46. (New) A display apparatus according to claim 40, wherein said image processing circuit transmits simultaneously a set of video signals adjacent even and odd rows to said liquid crystal panel.

47. (New) A display apparatus according to claim 40, wherein a set of video signals are written into said liquid crystal panel at the same time.

48. (New) A display apparatus according to claim 46, wherein a driving line is disposed between adjacent even and odd rows, and signal lines for odd and even rows are arranged in a Herwatingly manner.

49. (New) A display apparatus according to claim 40, further comprising wireless interfaces.

50. (New) An active matrix liquid crystal display apparatus, comprising:  
a device substrate provided with pixel electrodes and switching devices arranged on respective sites defined by data signal wirings and scan signal wirings, and an opposite substrate provided with opposite electrodes arranged on the respective sites located directly opposite to the corresponding pixel electrodes, with  
said scan signal wirings being grouped into alternately arranged first and second scan signal wirings,  
first pixel electrodes selected by said first scan signal wirings and second pixel electrodes selected by said second scan signal wirings being spatially displayed from each other, and  
said data signal wirings being grouped into alternately arranged first and second data signal wirings, and with  
said first pixel electrodes being connected to said first data wirings by way of said switching devices, said second pixel electrodes being connected to said second data wirings by way of said switching devices; and  
first and second transfer switches being driven by a same shift register, said first data wirings being connected to an image signal input terminal through said first transfer switch, said second data wirings being connected to said image signal input terminal through said second transfer switch and an image signal delay circuit, and said image signal delay circuit having means for delaying an image signal according to the spatial displacement between said first and second pixel electrodes.

51. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein said switching devices operate as three terminal type transistors and said opposite electrodes are a common electrode.

52. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein said switching devices operate as two terminal type diodes and said opposite electrodes operate for said scan signal wirings.

53. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein said first data signal wirings and said second signal wirings are formed in a same layer.

54. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein said first data signal wirings and said second data signal wirings are formed in different layers.

55. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein said pixel electrodes are formed on a front surface of an insulation layer, and said switching devices are formed on a rear surface of said insulation layer.

56. (New) An active matrix liquid crystal display apparatus according to claim 50, wherein reflection electrodes are arranged on either of said device substrate or said opposite substrate.

57. (New) An active matrix liquid crystal display apparatus according to claim 50, further comprising peripheral circuits for applying a voltage to said switching devices arranged on said device substrate, with said peripheral circuits made of monocrystalline Si and said switching devices made of polycrystalline Si.

58. (New) An active matrix liquid crystal display apparatus according to claim 50, further comprising a thin film having a tensile stress for supporting said switching devices.